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"no restriction is placed on positions of the clocked logic circuit and the non-clocked logic circuit"

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Method of fabricating an integrated circuit through utilizing ...

The method includes forming a plurality of **basic units** with the same component ... layout layer to program the **basic units** for building a **clocked logic circuit**.

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Method of fabricating an integrated circuit through utilizing ...

The method of claim 1 wherein step (b) utilizes the layout layer to program a plurality of **basic units** for generating the **clocked logic circuit**

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METHOD OF FABRICATING AN INTEGRATED CIRCUIT THROUGH UTILIZING ...

The method includes forming a plurality of **basic units** with the same ... the **basic units** for building a **clocked logic circuit** and a non-**clocked logic** ...

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Logic circuit and method of logic circuit design - Patent 20040130349

[0159] Given a **clocked logic circuit** with known T.sub.cycle and T.sub.setup, ... **basic units**, while the comparison data is transmitted through the units. ...

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